Application No.: 10/812,471

Examiner: Ernest Unelus

Art Unit: 2187

LIST OF CURRENT CLAIMS

1. (Currently Amended) A memory card having a plurality of different interfaces,

comprising:

a function module;

a controller coupled to the function module for accessing;

a first buffer coupled to the controller for sending a first control signal, the first

buffer including a first pull-down resistor for providing a first voltage level; and

a second buffer coupled to the controller for sending a second control signal, the

second buffer including a second pull-up resistor for providing a second voltage level,

wherein the memory card activates uses the pull-down and the pull-up resistors to

separate two of the interfaces by activating one of the interfaces for detecting the first

voltage level of the first buffer when the memory card is inserted into a first card reader,

and such that the first buffer is activated to send the first control signal if the detection of

the first voltage level of the first buffer is positive; or the memory card activates by

activating another interface for detecting the second voltage level of the second buffer

when the memory card is inserted into a second card reader, and such that the second

buffer is activated to send the second control signal if the detection of the second voltage

level of the second buffer is positive.

2. (Original) the memory card as claimed in claim 1, wherein the one of the

interfaces is for a SD card.

3. (Original) The memory card as claimed in claim 1, wherein the first voltage

level is a high voltage level.

4. (Original) The memory card as claimed in claim 1, wherein the first control

signal is a CMD signal.

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5. (Original) the memory card as claimed in claim 1, wherein the another interface is for a MS card.

- 6. (Original) The memory card as claimed in claim 1, wherein the second voltage level is a lower voltage level.
- 7. (Original) the memory card as claimed in claim 1, wherein the second control signal is a BS signal.